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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority APPLICATION SERIAL NO. .... 08/993,663  
Priority FILING DATE ..... December 18, 1997  
INVENTORSHIP ..... Salman Akram et al.  
ASSIGNEE ..... Micron Technology, Inc.  
Priority GROUP ART UNIT ..... 2812  
Priority EXAMINER ..... S. Mulpuri  
ATTORNEY'S DOCKET NO. .... MI22-1171  
TITLE: Methods of Forming a Transistor Gate (As Amended)

INFORMATION DISCLOSURE STATEMENT


PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation application of co-pending application Serial No. 08/993,663, filed December 18, 1997. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Respectfully submitted,

Dated: April 14, 1999

Attorney:   
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